

Recent VLBI Activities at CRL

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Communications Research Laboratory
Japan



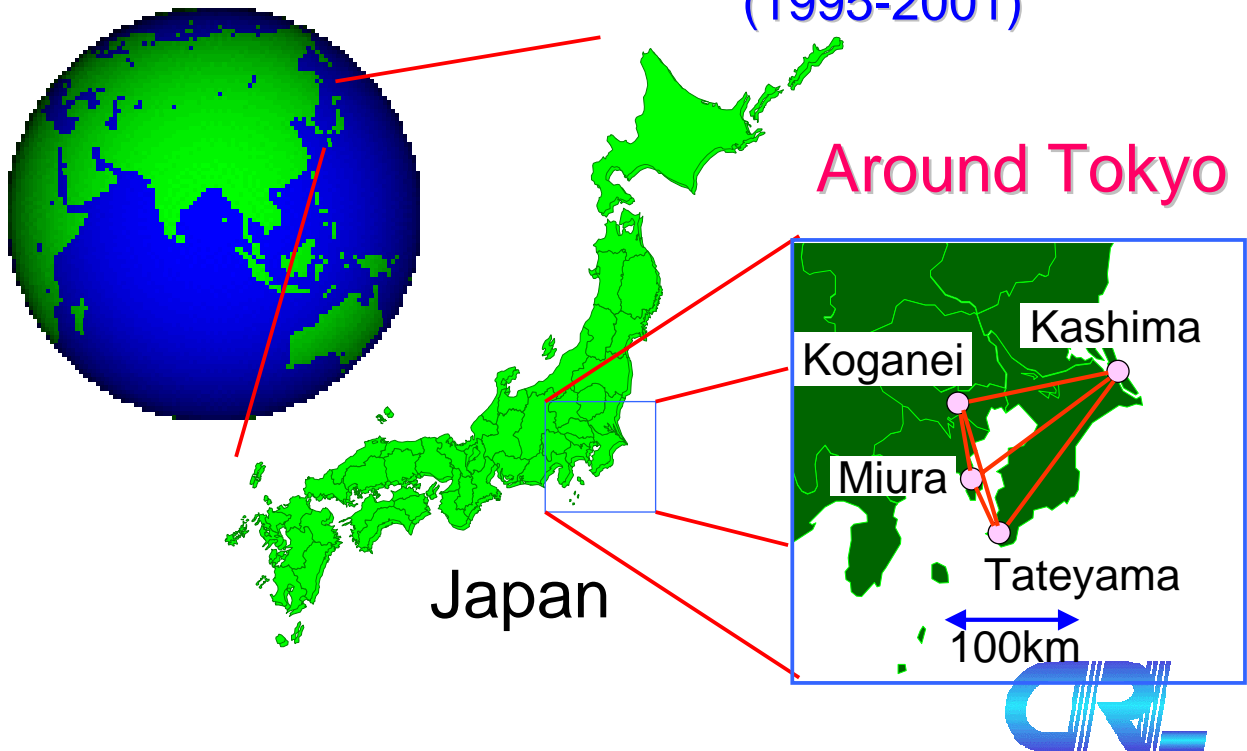
Recent VLBI Activities at CRL

- Detection of unusual crustal deformation by Keystone (KSP) VLBI network
- Giga-bit VLBI observations
 - success in 1 Gbps real-time VLBI
 - success in 2 Gbps tape-based VLBI
- Development of the Internet VLBI
- Development of the VLBI Standard Interface (VSI) system



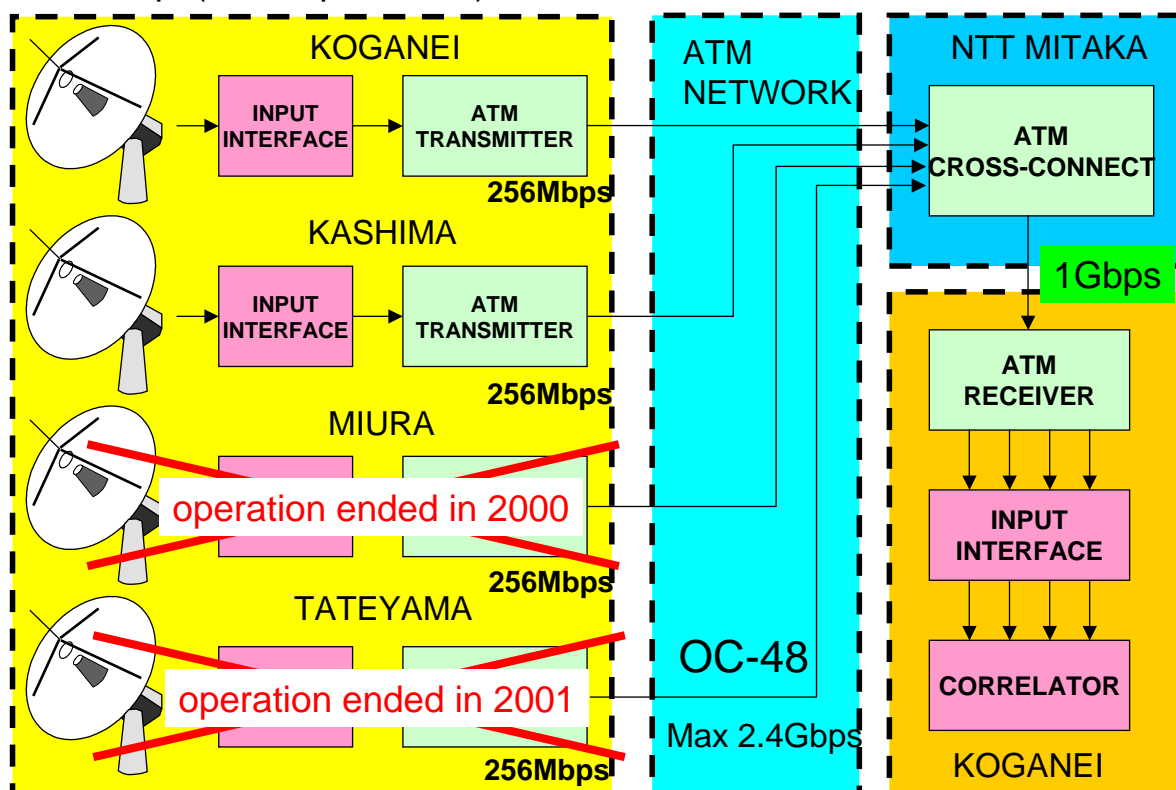
KSP VLBI network

(1995-2001)



Real-Time VLBI System

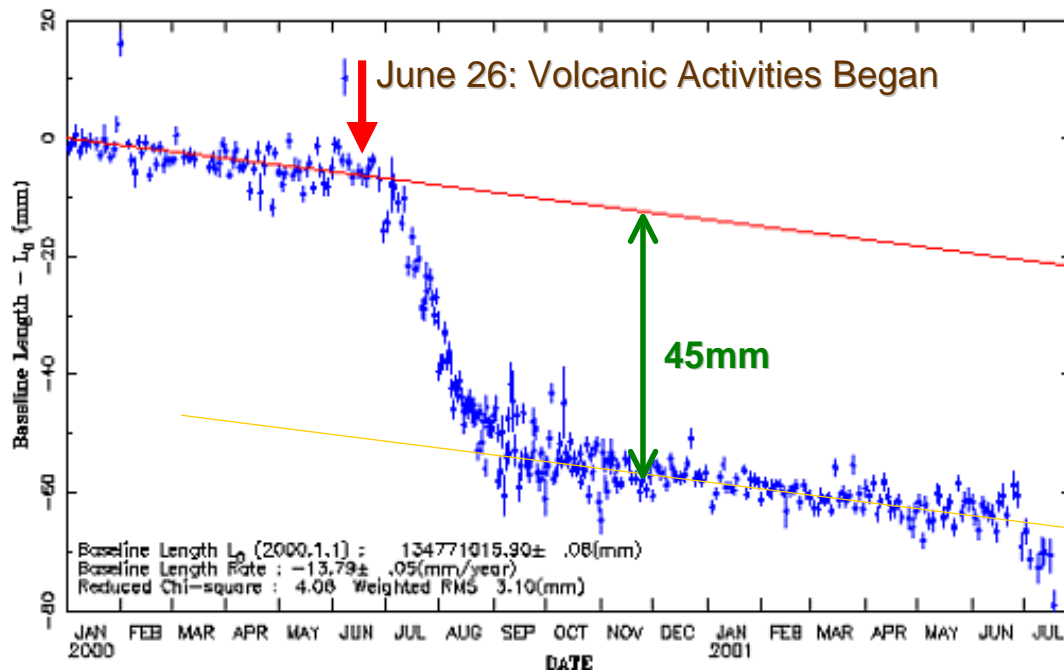
256Mbps(=16Mbps × 16ch)/station



Kashima-Tateyama

KASHIM11-TATEYAMA

20-Jul-01 02:56:24 (JST)



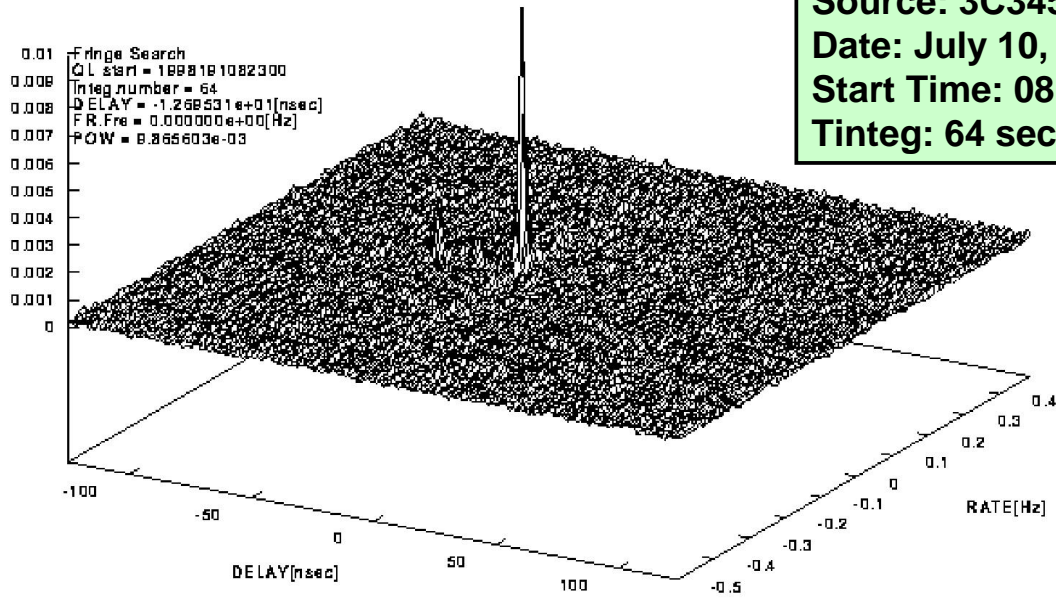
Giga-bit VLBI System as of 2000



The First Fringes

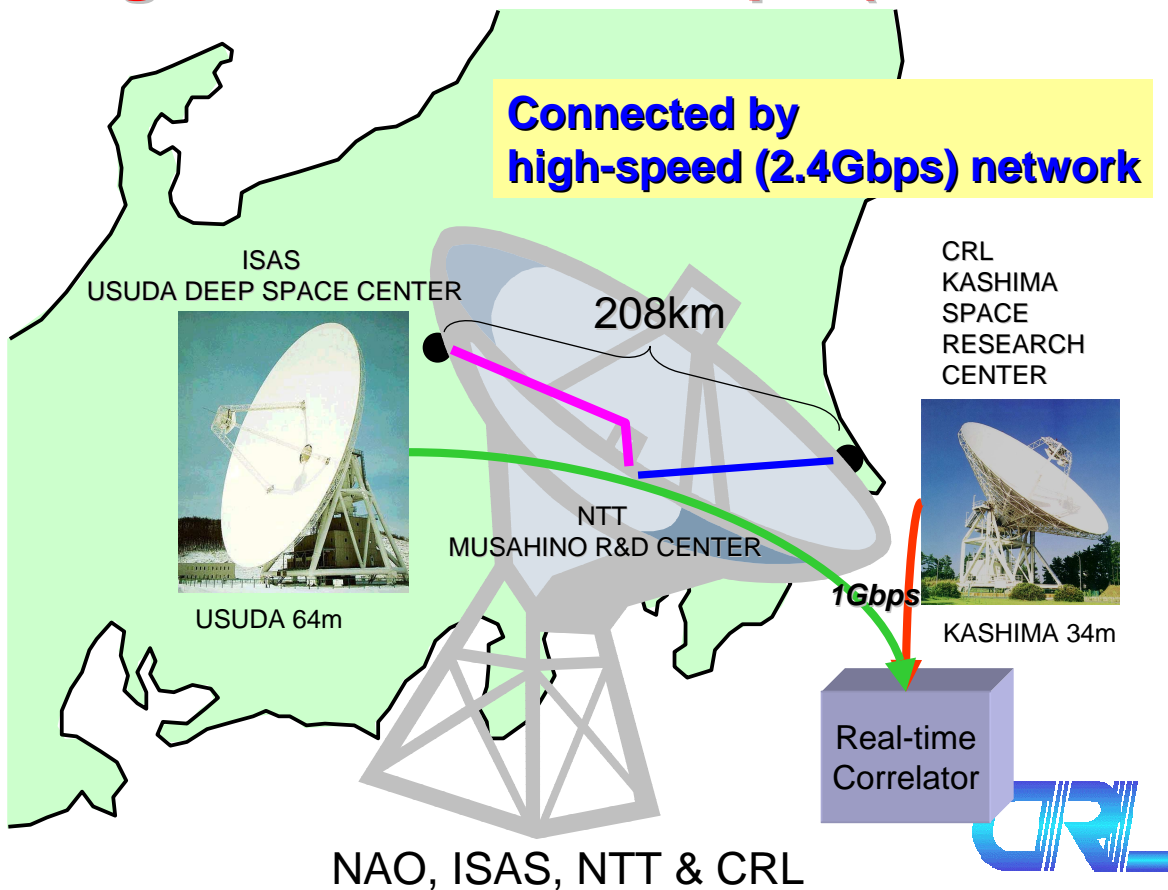
AMPLITUDE

Fringe Search
CL start = 1998191082300
Integ number = 64
DELAY = -1.268531e+01[nsec]
FR.Fre = 0.000000e+00[Hz]
PCW = 8.866603e-03

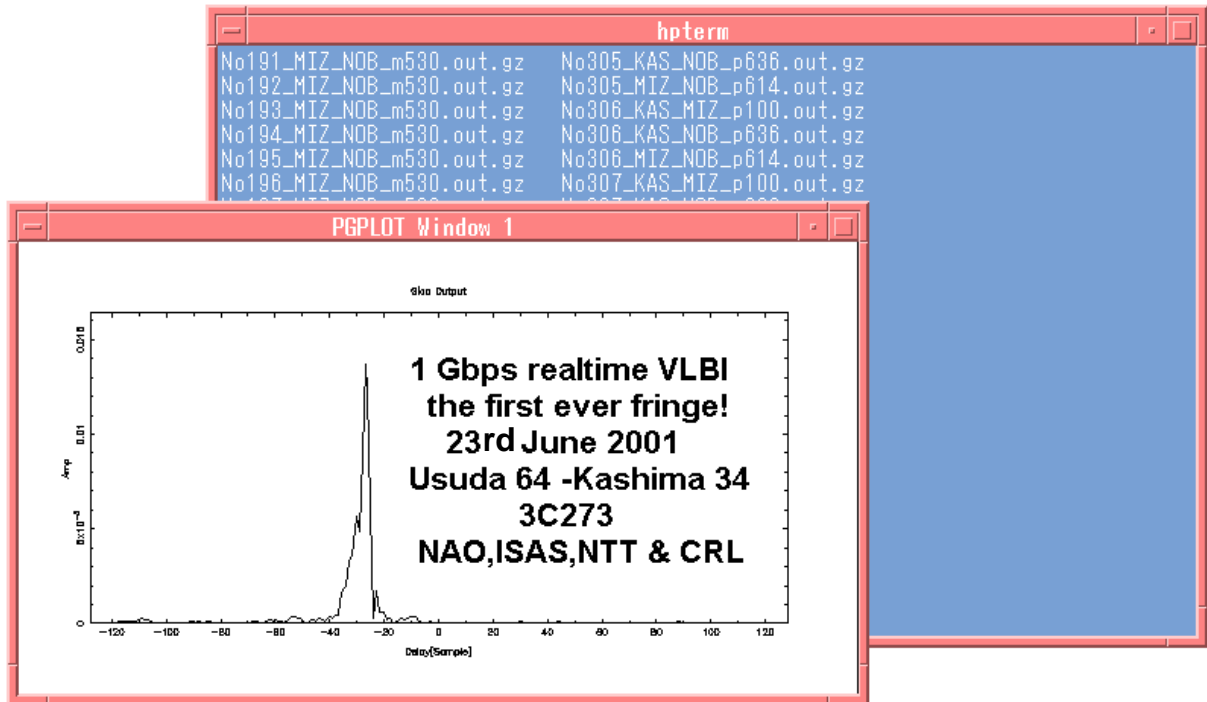


Kashima11-Kogane
Source: 3C345
Date: July 10, 1998
Start Time: 08:20:00
Tinteg: 64 sec

Large Virtual Telescope (GALAXY)

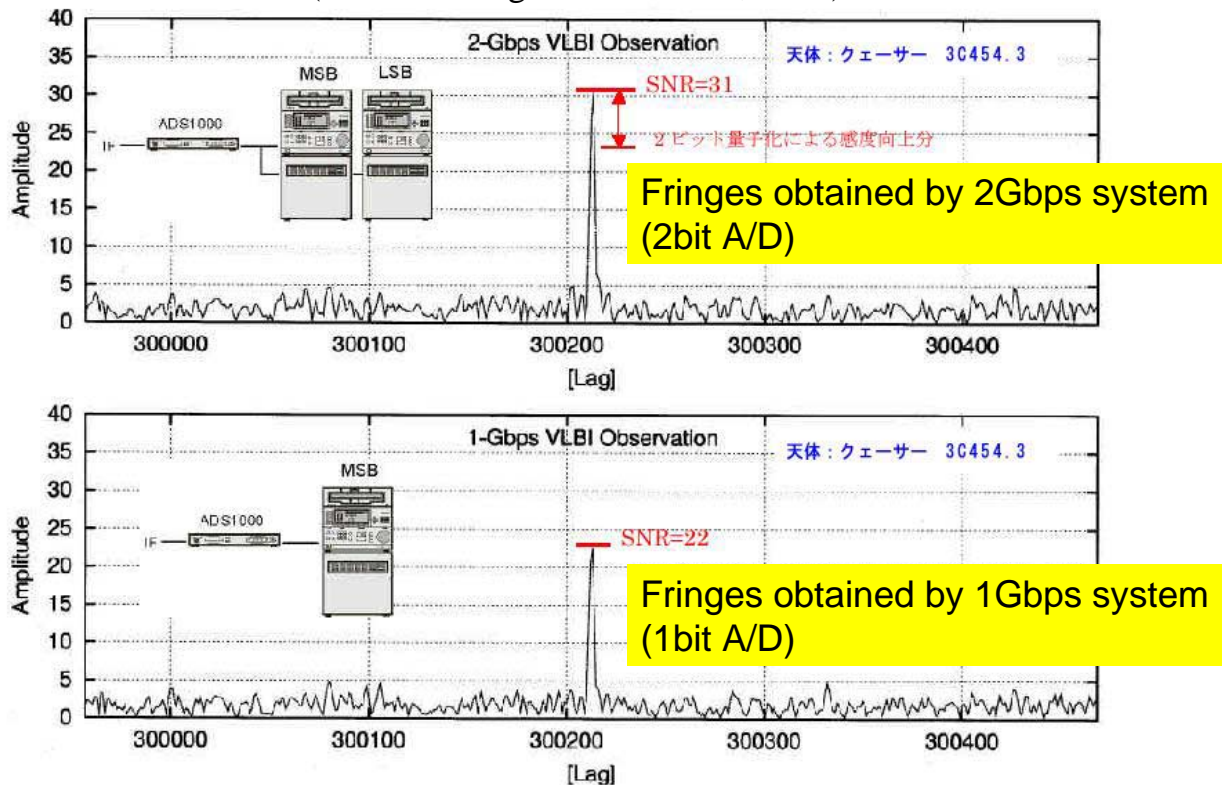


Successful Detection of 1 Gbps real-time VLBI fringes

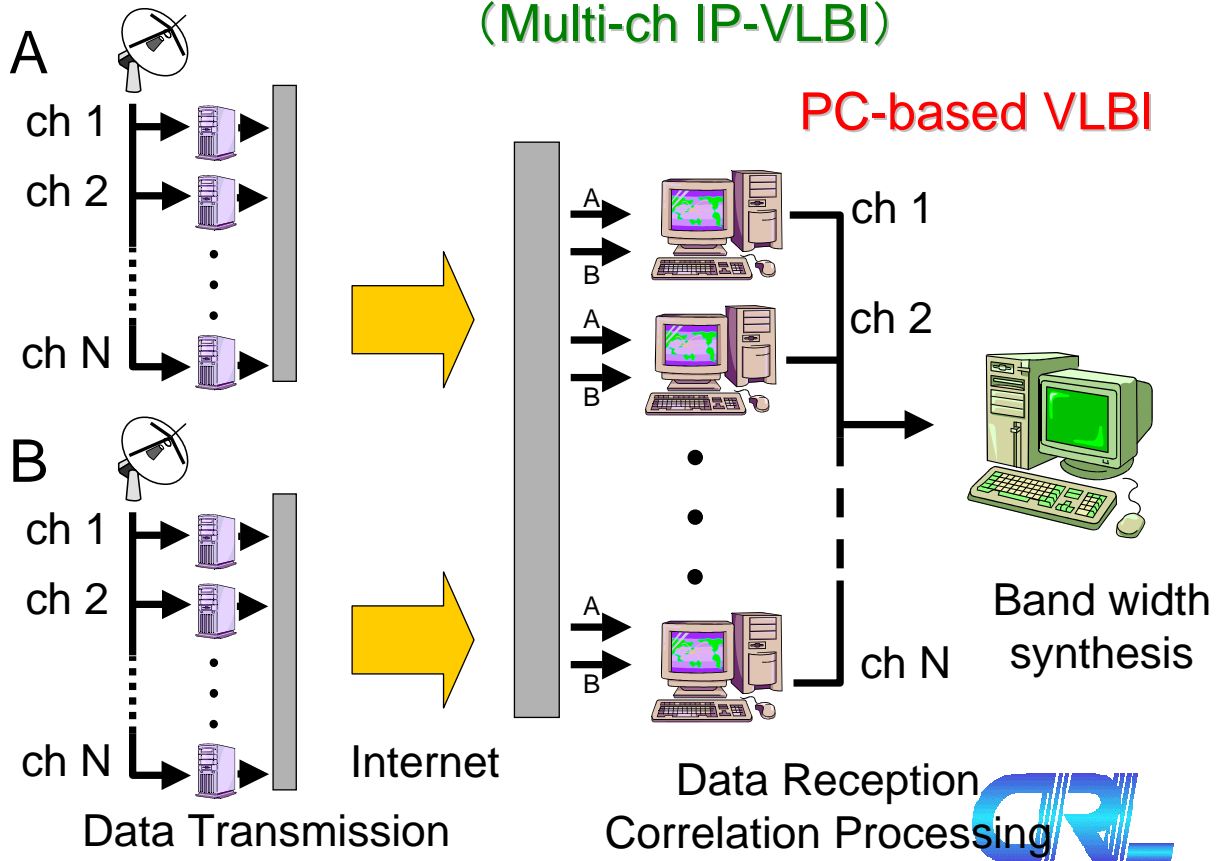


Success in 2Gbps VLBI (tape based)

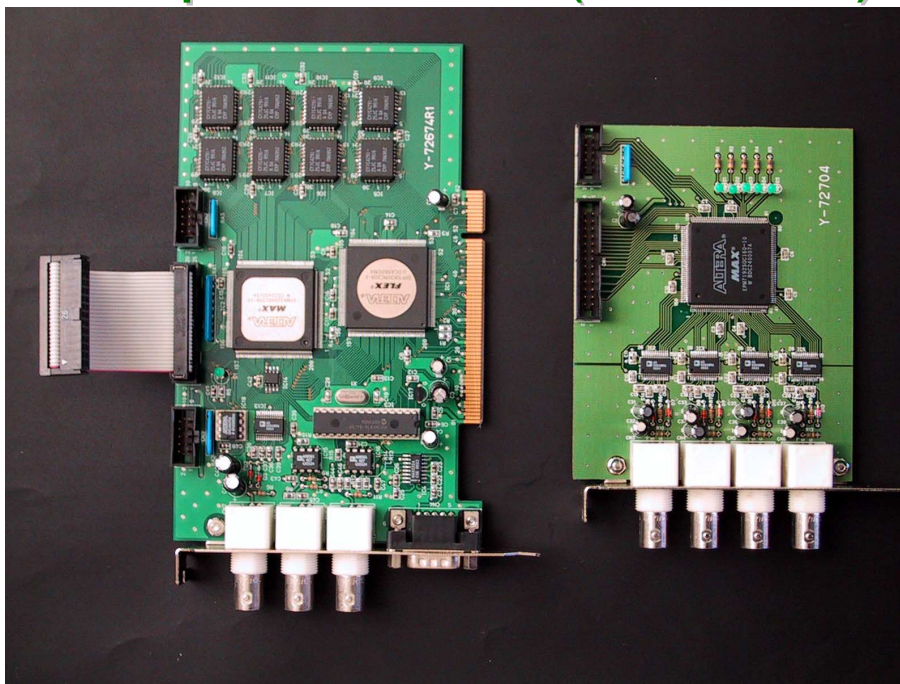
(Kashima-Koganei Dec.12, 2001)



IP-VLBI for Geodetic Use (Multi-ch IP-VLBI)



IP-VLBI Sampler Board (PCI Bus)

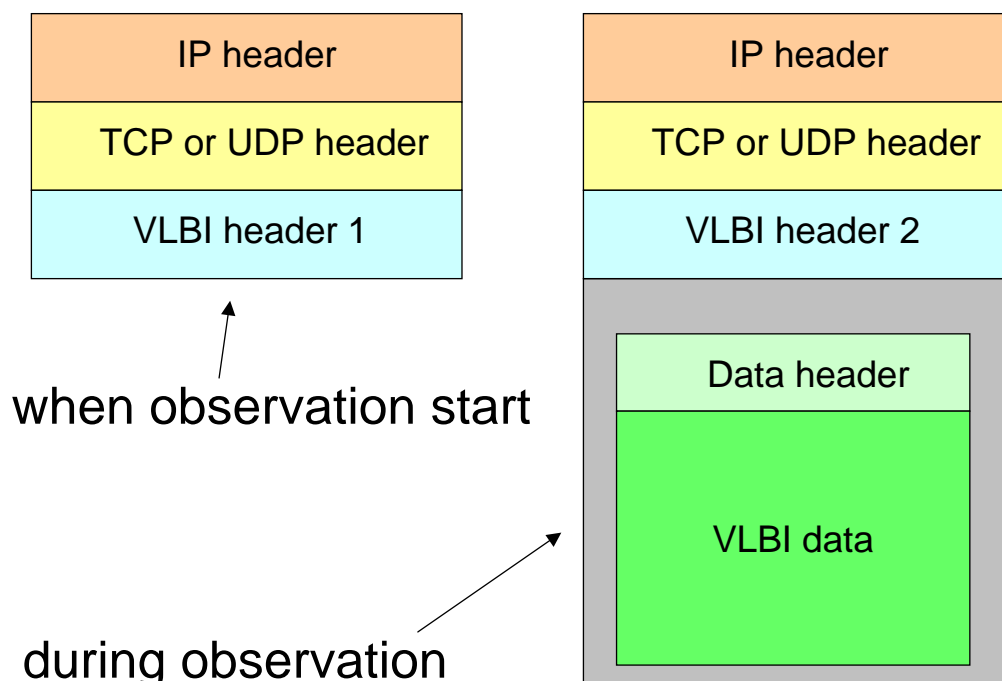


Specifications of Sampler Board

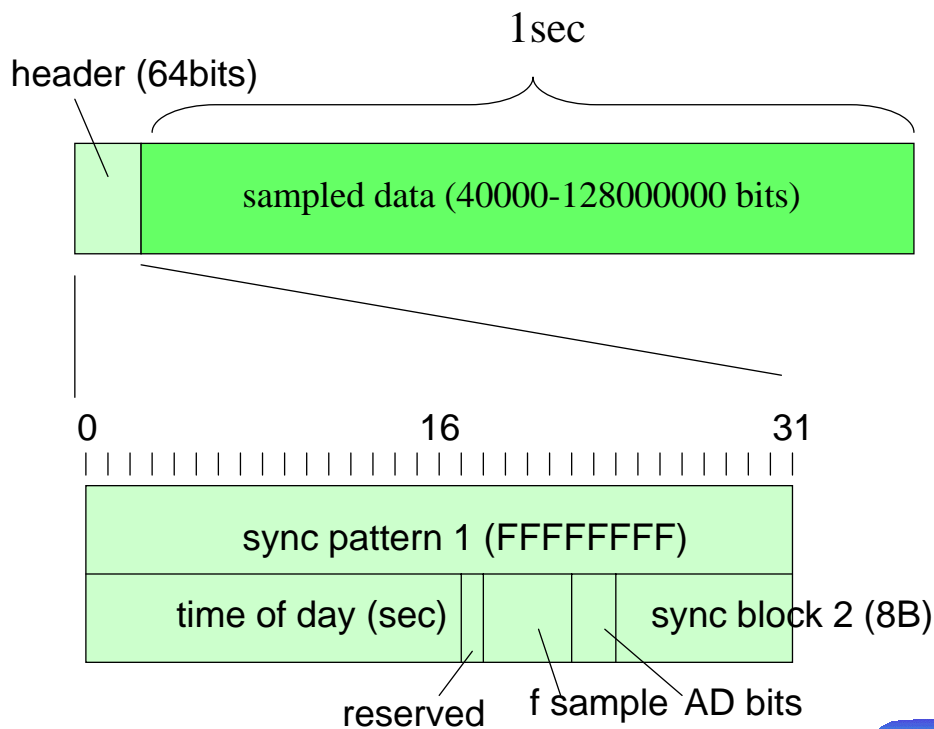
Reference signals	10MHz (+10dBm), 1PPS
Number of INPUT CH	1 : main board only 4 : with auxiliary board
A/D	1, 2, 4, 8 bits
Sampling Freq.	40kHz, 100kHz, 200kHz, 500kHz, 1MHz, 2MHz, 4MHz, 8MHz, 16MHz



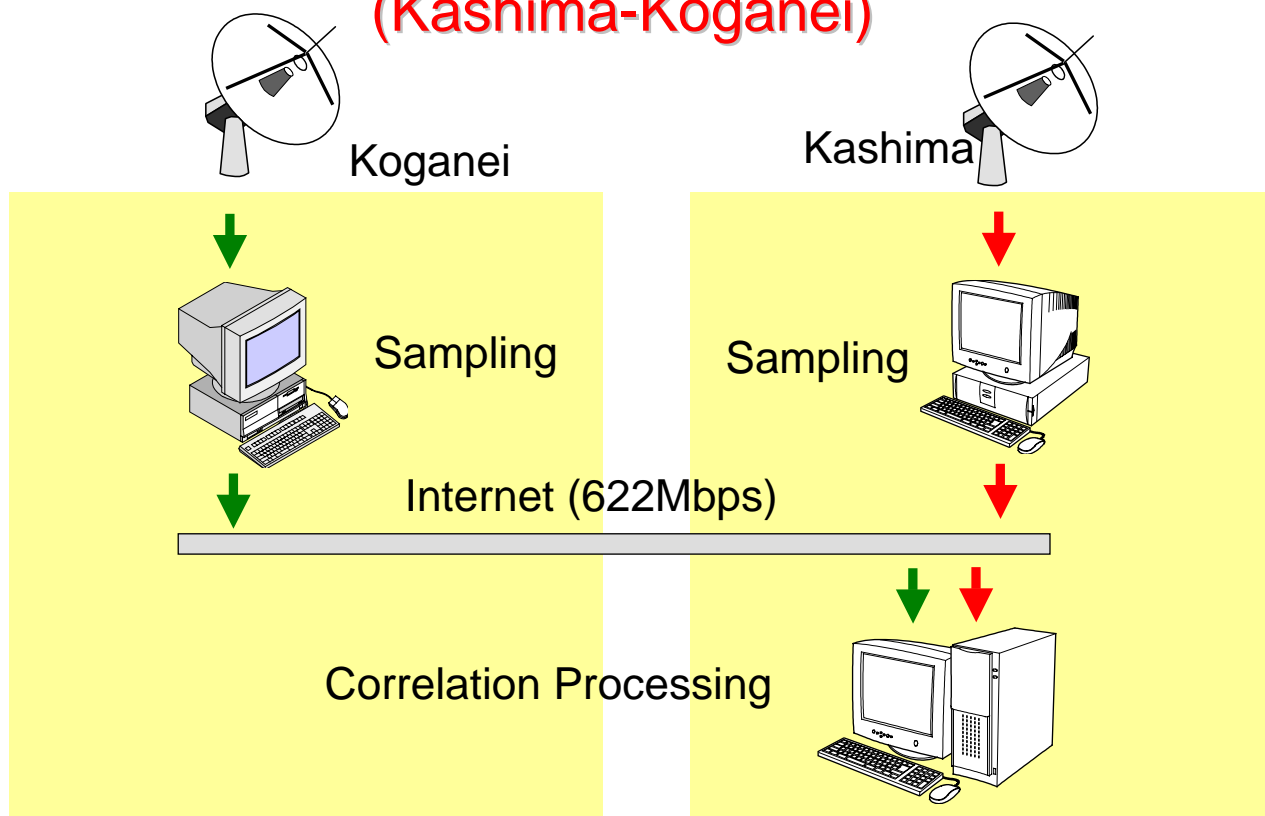
IP Data Format



IP-VLBI Data Format

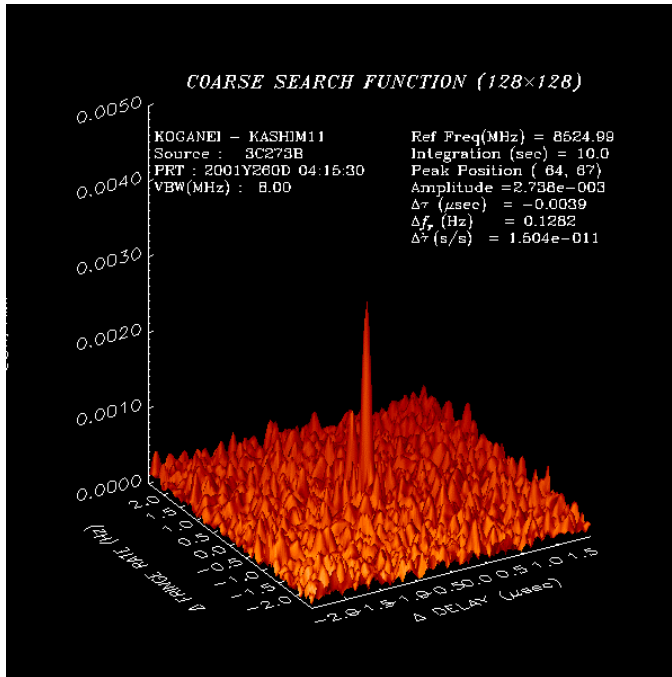


IP-VLBI Test Observation (Kashima-Koganei)



IP-VLBI Sampler Board Test

First Fringes on Kashima-Koganei



Sept. 17, 2001

3C273B

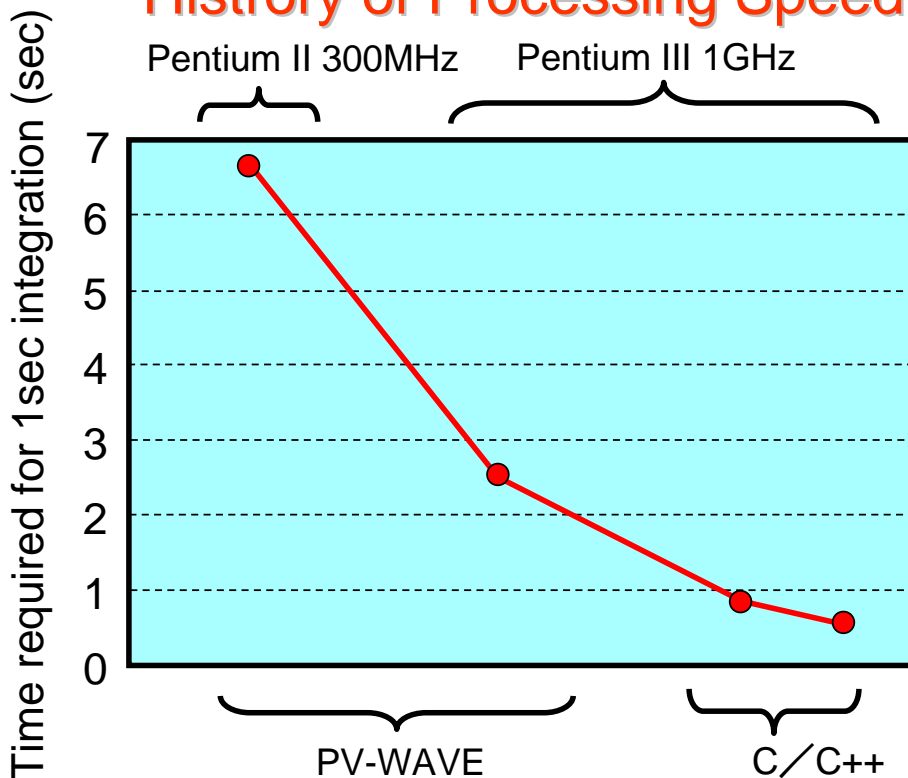
10sec integration

Note!
 This is off line
 processing

16MHz sampling



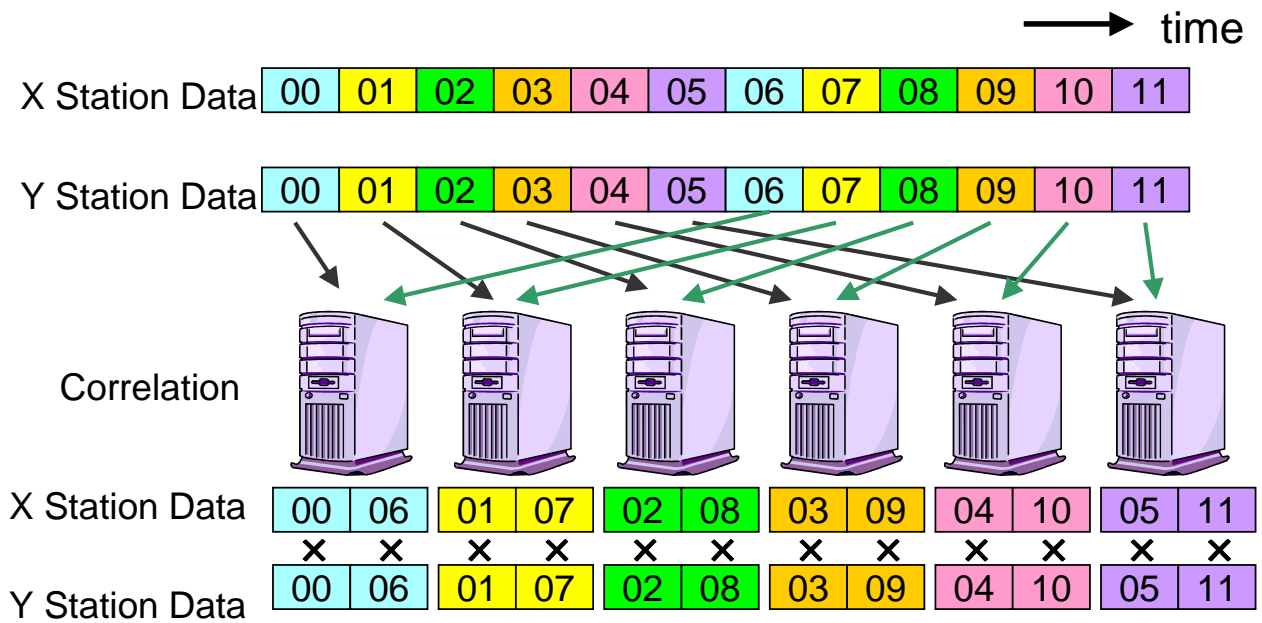
History of Processing Speed



4MHz Sampling • 16 lag



IP-VLBI Time Distributed Processing

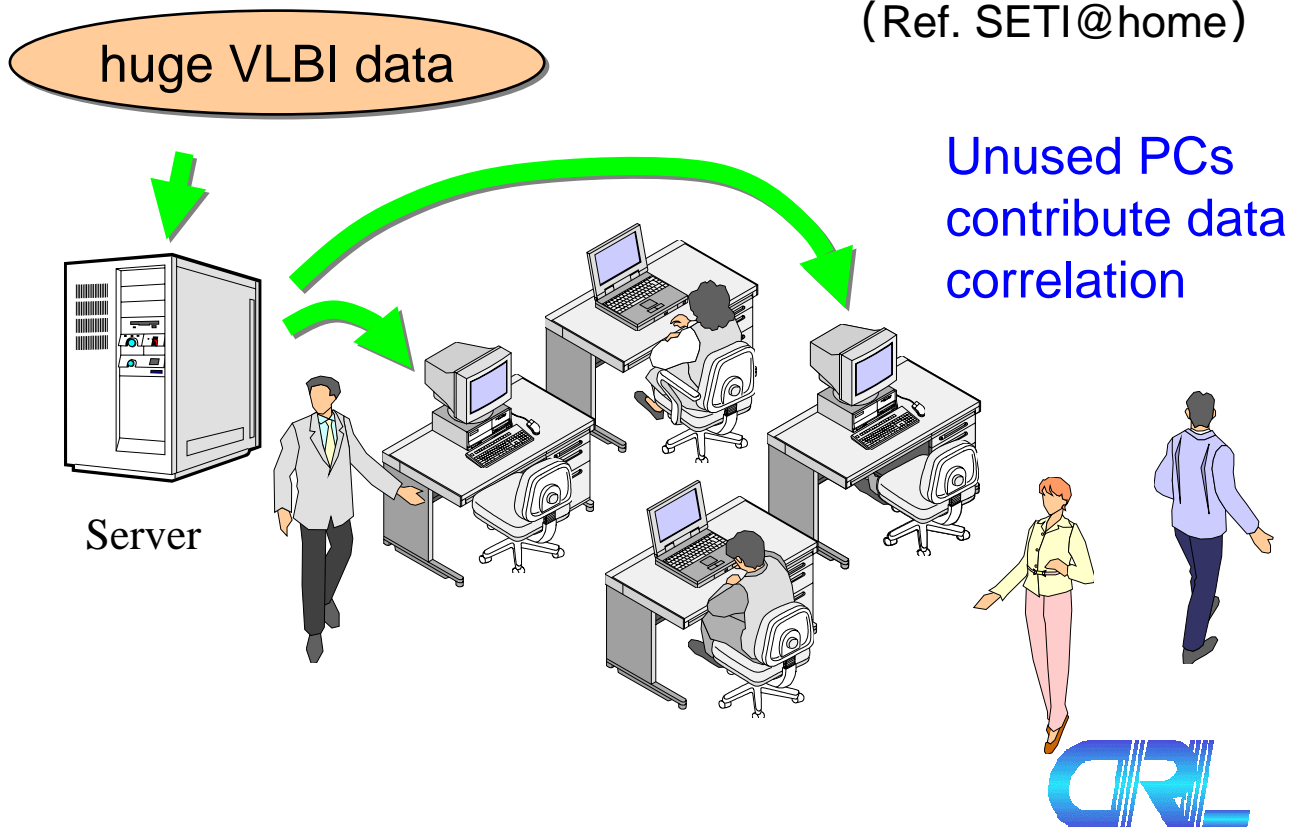


If processing time for 1sec data requires 5 seconds,
use of more than 6 PCs enables us to process
in quasi real-time.

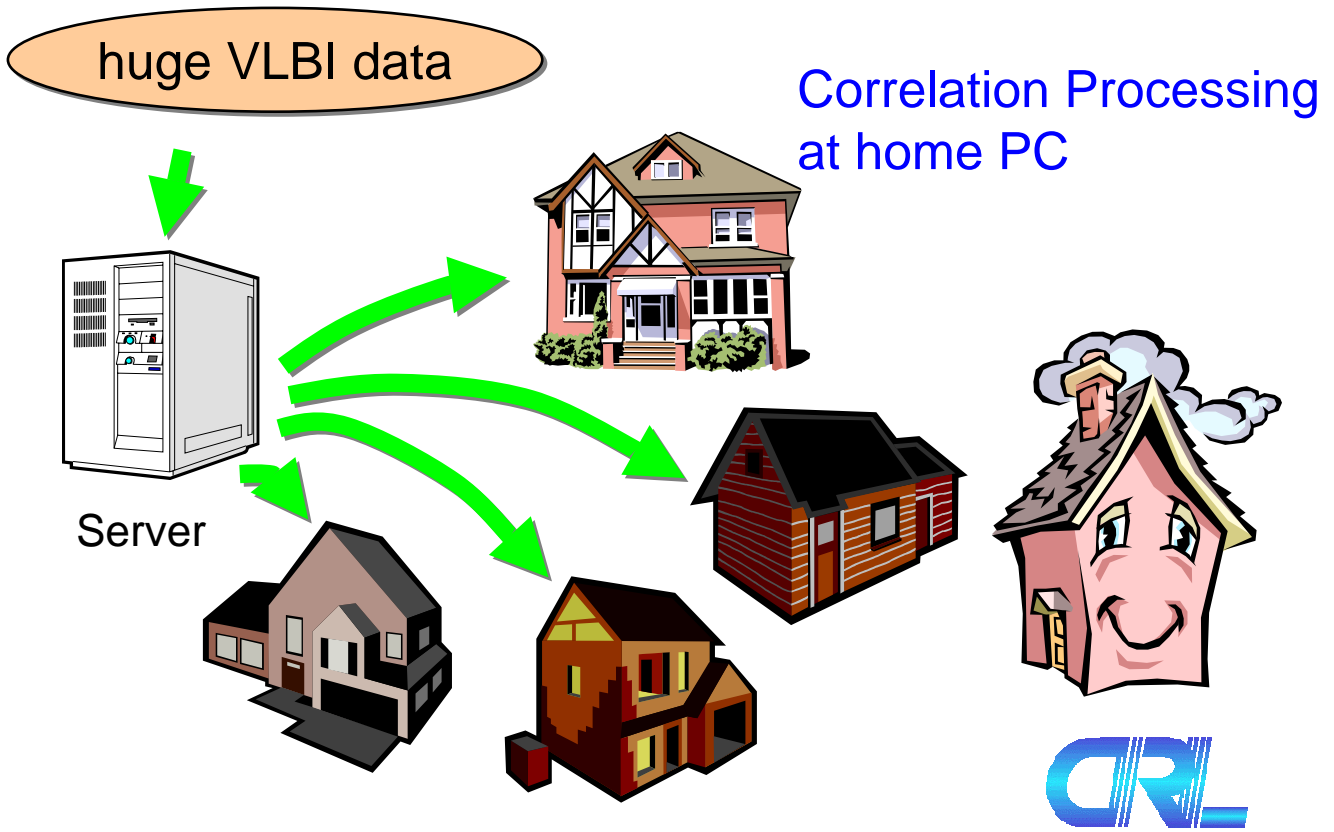


VLBI@office

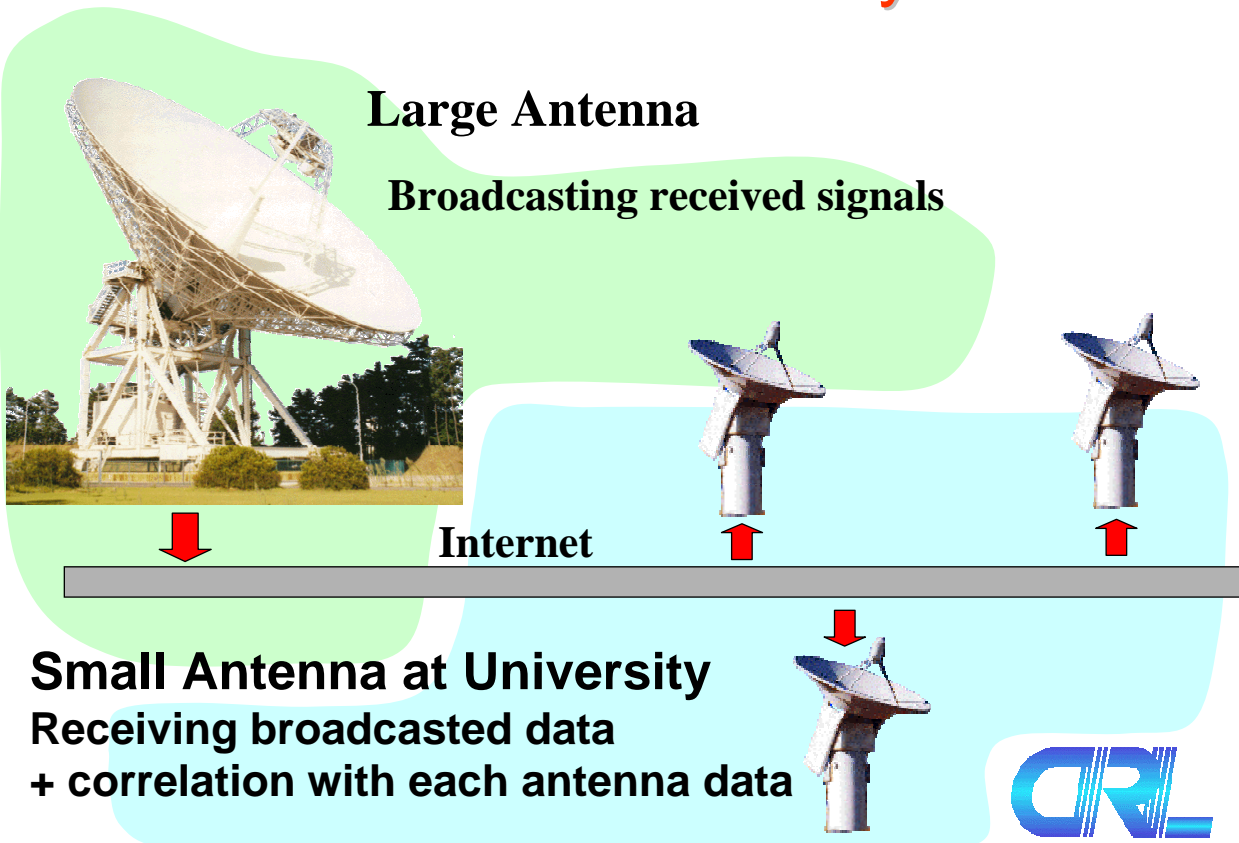
(Ref. SETI@home)



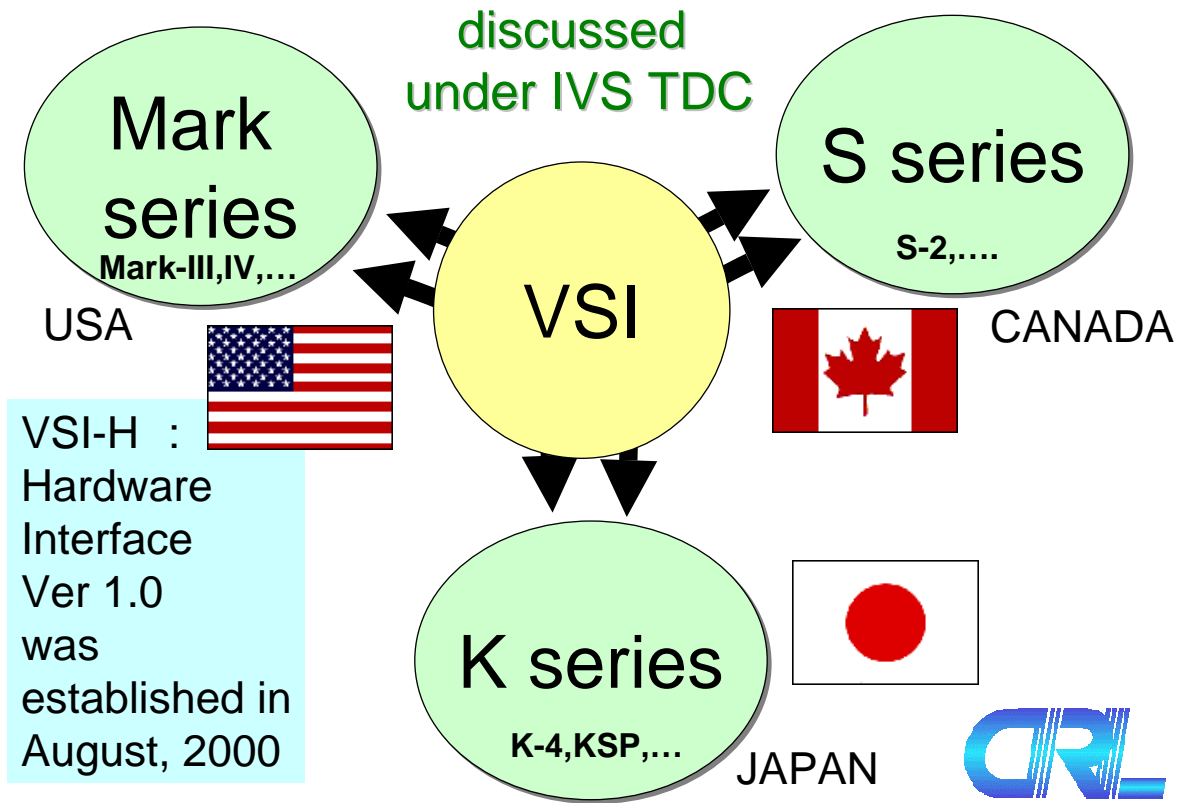
VLBI@home



VLBI@university



VLBI Standard Interface (VSI)



VSI Gbit recorder

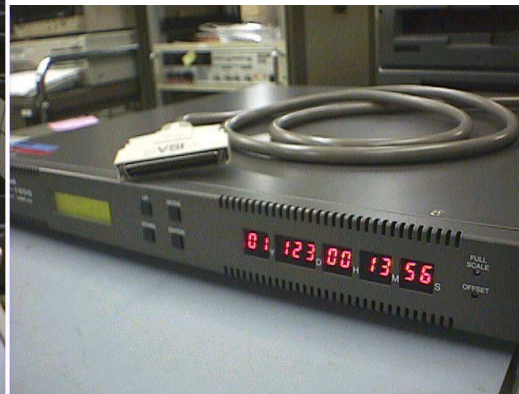


VSI interface in it!

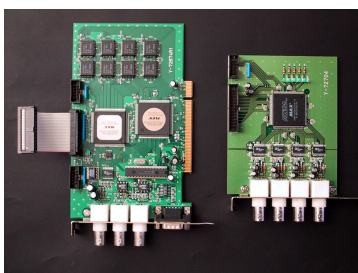


VSI AD sampler

- ADS1000 Small 2 Gbps AD sampler (VSI-DAS)
- 1/10 size, 1/5 cost, reduced AD jitter by PLO



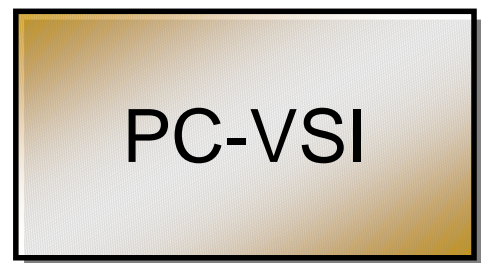
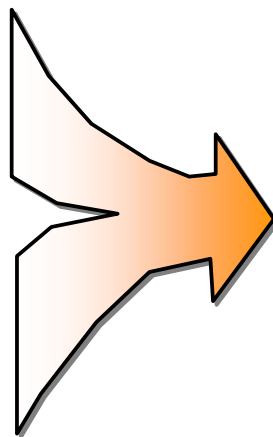
Future Plan



IP-VLBI



Gigabit VLBI



IVS 2002 General Meeting



Second IVS General Meeting February 4-6, 2002 Tsukuba, Japan

hosted by GSI and CRL

<http://ivscg.gsfc.nasa.gov/>







IVS Receives Award from Japanese Ministry on "RADIO DAY" (June 1, 2001)



Ministry of Public Management, Home Affairs, Posts and Telecommunications



Conclusions

-  VLBI observations to evaluate a GPS time and frequency reference receiver were carried out on the Kashima-Koganei baseline.
-  Good correlation (fringe) was successfully detected at 8 GHz as well as 2 GHz.
-  Integration up to 100 seconds was available by using a third-order fringe search method.
-  These results are as same as those expected from a phase stability measurement result before VLBI observations.

