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Subject No.	2026R-55	
Job Title	Fixed Term Researcher	
Department	Cybersecurity Research Institute / Cybersecurity Laboratory	
Work Contents (Research theme)	Research on Hardware Security	
Detail of Work Contents	Research and development of security technologies targeting hardware such as IoT devices, FPGA (Field Programmable Gate Arrays), and sensor devices, as well as the firmware installed on these devices. Examples of research themes: • Vulnerability verification Technologies for IoT devices • Technologies for verifying the existence of backdoors in IoT devices • Technologies for verifying unauthorized behavior in FPGA boards • Security verification Technologies for connected cars At the Cybersecurity Research Lab, researchers are encouraged to approach their research themes autonomously, leveraging their creativity and capabilities. They are free to set their own themes as long as they do not significantly deviate from the lab's mission and the scope of the job description. When considering research proposals, we recommend reviewing the ongoing research and development efforts of the Cybersecurity Research Lab through our website or related published papers. Website URL: https://csl.nict.go.jp/ The Article 15(2) of the Act on the activation of Science, Technology and Innovation will be applied to this work content. Employees to be hired through this recruitment may apply for external competitive research fundings such as Grant-in-Aid for Scientific Research (KAKENII) and NICT's internal research fundings.	
Application requirement	[Required Experience and Skills] • Ph.D. or Master's degree (including those expected to obtain one) • Proven research experience related to the field of cybersecurity • Proficiency in English for information gathering and academic writing • Good communication skills to collaborate effectively with internal and external partners [Preferred Experience and Skills] • Experience presenting at international conferences or journals related to the field of cybersecurity • Research experience related to hardware (such as integrated circuits and FPGA) • Practical experience in hardware or firmware analysis • Ability to read, write, and communicate in Japanese [Ideal Candidate Profile] • Someone who finds enjoyment in developing and pursuing their own research themes • Someone resilient and willing to put in the effort to take on high-level challenges • A person with curiosity who is eager to explore unknown areas • Someone who resonates with the spirit of "Sharing is Caring" and values teamwork and collaboration	
Recruiting (Number of people)	1	
Contract period	hiring date ~ March 31,2027 N.B. Contract could be renewed.	
The employment period in case of fully Up to 5 years if certain conditions are fulfilled renewing		
Salary (basic salary)	¥515, 000 ~ ¥534, 000/month Basic salary shall be determined by taking into account each employee's experience and task to be engaged in. However, as a basic salary is compliant with government employees' wages, it shall be changed when a basic salary is changed after labor union and the like of NICT agreed under a revision to the government employees' wages.	
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^{*} Scope of change in work and workplace: No changes are expected in general

* The expressions used in "Work Contents (Research theme)" and "Detail of Work Contents" and the names of
"Department" and "Work Place" may be subject to change due to organizational restructuring or other reasons.